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Attorney Docket Number	50470/DBP/B600
Application Number	09/892,310
Filing Date	June 26, 2001
Applicant(s)	Zheng Qi, et al.
Group Art Unit	2131
Examiner Name	not yet assigned

U.S. PATENT DOCUMENTS

EXAMINER INITIALS	Cite No. <sup>1</sup>	DOCUMENT NUMBER Number - Kind Code <sup>2</sup> (If Known)	PUBLICATION DATE MM-DD-YYYY	NAME OF PATENTEE
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FOREIGN PATENT DOCUMENTS

EXAMINER INITIALS	Cite No. <sup>1</sup>	Foreign Patent Document Country Code <sup>3</sup> - Number <sup>4</sup> - Kind Code <sup>5</sup> (If Known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	T <sup>6</sup> (✓)
35	EP	0 661 843 A2	07-05-1995	Alvarez	
35		EP 0 661 843 A3	11-10-1999	Alvarez	

OTHER DOCUMENTS

EXAMINER INITIALS	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article, title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
35		European Search Report dated January 9, 2003 from corresponding European Patent Application No. 01309323.2-1525, filed November 2, 2001
35		JENKINS, et al.; <i>I.C. multiplexer increases analogue switching speeds</i> ; Electronic Engineering; Vol. 45, No. 540; February 1973; pp. 73-75; XP-000807191
35		MCLOONE et al.; <i>A High Performance FPGA Implementation of DES</i> ; 2000 IEEE Workshop On Signal Processing Systems, SIPS 2000. Design and Implementation (CAT. NO. 00TH8528), October 2000; pp. 374-382 XP-002251022

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EXAMINER SIGNATURE	<i>Shen</i> <i>Shen</i>	DATE CONSIDERED	9/20/04, 9/23/04
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